

<b>GNITS</b>	<b>GNITS-D / ECE / TTL / 041 / 00</b>
<b>YEAR WISE TIMETABLE</b>	<b>DEPARTMENT: ECE</b>

Branch: ECE		Course: B. Tech		Year: 2/4 A		Semester: II		Room No.: D102		w.e.f: 19-02-2024	
<b>Time / Day</b>	<b>1</b> 9:00 - 10:00	<b>2</b> 10:00 -11:00	<b>3</b> 11:10-12:10	<b>4</b> 12:10 PM - 1:00 PM	<b>5</b> 1:00-2:00	<b>6</b> 2:00-3:00	<b>6</b> 3:00-4:00				
<b>Mon</b>	Placement Training		EMTL*/CSE*	<b>L U N C H</b>	AC Lab(A1)/DELD Lab(A2)/MNPI(A3)						
<b>Tue</b>	AC Lab(A2)/DELD Lab(A3)/MNPI(A1)				PTSP	EMTL	HVPE				
<b>Wed</b>	DELD	DELD	PTSP		AC	CSE	CSE				
<b>Thu</b>	EMTL	AC	CSE		AC Lab(A3)/DELD Lab(A1)/MNPI(A2)						
<b>Fri</b>	PTSP	DELD	AC		EMTL	Placement Training					
<b>Sat</b>	AC*	DELD*	HVPE		SPDC	VALUE ADDED COURSE					

(\* ) = Tutorial      17 Theory hours + 9 Lab hours      Note: 11:00 AM to 11:10 AM – Break

Subject	No. of Classes	Faculty	Lab Batches
Probability Theory and Stochastic Processes(BS124BN)	3	Dr. Renuka Methre	A1 Batch- 22251A0401 – 422,23255A0401-402
Electromagnetic Theory and Transmission Lines(PC124BJ)	3	Y.Prakash	A2 Batch- 22251A0423 – 444,23255A0403-404
Analog Circuits(PC124AZ)	3	M.lakshmi	A3 Batch- 22251A0445 – 464,23255A0405-406
Digital electronics and Logic Design(PC124BE)	3	P.Madhuri	
Control System Engineering(PC124BC)	3	V.PurnaChandraReddy	
Analog Circuits Lab(PC12418)	1.5	M.lakshmi (A1,A3) P.Satyanarayana Goud(A2)	
Digital electronics and Logic Design Lab(PC12419)	1.5	V.Shankar(A1),P.Madhuri(A2,A3)	
Mini Project I(PW12424)	2	L.Nagaraju,T.SriLatha,B.TulasiSowjanya	
Human Values and Professional Ethics(MC12422)	2	Dr.T.Anuradha	
Skill and Personality Development	1	R.Elizabeth Kavita	

\*EMTL--2<sup>nd</sup> 4<sup>th</sup> Weeks, CSE-1<sup>st</sup>,3<sup>rd</sup> and 5<sup>th</sup> Weeks

Dept. Timetable Coordinator      Main Timetable Coordinator  
 Copy to: Individual Staff Member / HOD / Main Timetable Coordinator / Principal / OC

Head of the Department

Principal

GNITS	GNITS-D / ECE / TTL / 041 / 00
YEAR WISE TIMETABLE	DEPARTMENT: ECE

Branch: ECE Course: B. Tech Year: 2/4 B Semester: II Room No.: D104 w.e.f: 19-02-2024

Time / Day	1 9:00 - 10:00	2 10:00 - 11:00	3 11:10-12:10	12:10 PM - 1:00 PM	4 1:00-2:00	5 2:00-3:00	6 3:00-4:00
Mon	EMTL	AC	Placement Training		LUNCH	PTSP	CSE
Tue	HVPE	PTSP	CSE	L U N C H	AC Lab(B1)/DELD Lab(B2)/MNPI(B3)		
Wed	AC Lab(B2)/DELD Lab(B3)/MNPI(B1)				HVPE	Placement Training	
Thu	AC Lab(B3)/DELD Lab(B1)/MNPI(B2)				PTSP	DELD	AC
Fri	DELD	DELD*	AC		CSE	EMTL*/CSE*	EMTL
Sat	AC*	DELD	EMTL		SPDC	VALUE ADDED COURSE	

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Subject	No. of Classes	Faculty	Lab Batches
Probability Theory and Stochastic Processes(BS124BN)	3	B.TulasiSowjanya	B1 Batch - 22251A0465 - 486,23255A0407-408
Electromagnetic Theory and Transmission Lines(PC124BJ)	3	Dr.P.SaiSpandana	B2 Batch - 22251A0487 - 4A8,23255A0409-410
Analog Circuits(PC124AZ)	3	C.SridharBabu	B3 Batch - 22251A04A9 - 4C8,23255A0411-412
Digital electronics and Logic Design(PC124BE)	3	P.Lavanya	
Control System Engineering(PC124BC)	3	Ch.GanapathyReddy	
Analog Circuits Lab(PC12418)	1.5	P.SriPadma(B1,B2), N.Malathi(B3)	
Digital electronics and Logic Design Lab(PC12419)	1.5	T.SriLatha (B1), B.Tulasi Sowjanya (B2,B3)	
Mini Project I(PW12424)	2	DrB.Pavani,M.Lakshmi,V.PurnaChandraReddy	
Human Values and Professional Ethics(MC12422)	-	Dr.B.Rajeswari	
Skill and Personality Development	1	R.Elizabeth Kavita	

\*EMTL-2<sup>nd</sup>,4<sup>th</sup> Weeks, CSE-1<sup>st</sup>,3<sup>rd</sup> and 5<sup>th</sup> Weeks

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<b>YEAR WISE TIMETABLE</b>	<b>DEPARTMENT: ECE</b>

Branch: ECE	Course: B. Tech	Year: 2/4 C	Semester: II	Room No.: D206	w.e.f: 19-02-2024		
<b>Time / Day</b>	<b>1 9:00 – 10:00</b>	<b>2 10:00 –11:00</b>	<b>3 11:10-12:10</b>	<b>12:10 PM – 1:00 PM</b>	<b>4 1:00-2:00</b>	<b>5 2:00-3:00</b>	<b>6 3:00-4:00</b>
<b>Mon</b>	AC Lab(C1)/DELD(C2)/MNP1(C3)			<b>L U N C H</b>	HVPE	Placement Training	
<b>Tue</b>	EMTL	DELD	PTSP		AC	CSE	CSE
<b>Wed</b>	HVPE	EMTL	PTSP		AC Lab(C2)/DELD(C3)/MNP1(C1)		
<b>Thu</b>	PTSP	DELD	DELD*		AC	EMTL	EMTL*/CSE*
<b>Fri</b>	Placement Training		AC		AC Lab(C3)/DELD(C1)/MNP1(C2)		
<b>Sat</b>	DELD	CSE	AC*		SPDC	VALUE ADDED COURSE	

(\*) = Tutorial 17 Theory hours + 9 Lab hours Note: 11:00 AM to 11:10 AM – Break

Subject	No. of Classes	Faculty	Lab Batches
Probability Theory and Stochastic Processes(BS124BN)	3	A.SujathaReddy	C1 Batch- 22251A04C9 – 4E9,23255A0413-415
Electromagnetic Theory and Transmission Lines(PC124BJ)	3	L.NagaRaju	C2 Batch – 22251A04F0 – 4H0,23255A0416-418
Analog Circuits(PC124AZ)	3	V.Uma	C3 Batch – 22251A04H1 – 4K1,23255A0419-420
Digital electronics and Logic Design(PC124BE)	3	N.Harini	
Control System Engineering(PC124BC)	3	Ch.GanapathyReddy	
Analog Circuits Lab(PC12418)	1.5	V.Uma(C1), C.SridharBabu(C2), Y.Rakesh Kumar(C3)	
Digital electronics and Logic Design Lab(PC12419)	1.5	P.Roopa Ranjani(C1,C2), T.SriLatha (C3)	
Mini Project I(PW12424)	2	N.Malathi, P.Madhuri, G.Madhavi	
Human Values and Professional Ethics(MC12422)	-	Dr.T.Anuradha	
Skill and Personality Development	1	R.Elizabeth Kavita	

\* EMTL-2<sup>nd</sup>, 4<sup>th</sup> Weeks, CSE-1<sup>st</sup>, 3<sup>rd</sup> and 5<sup>th</sup> Weeks

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