

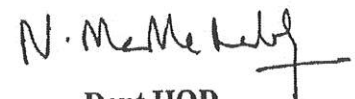
**G.NARAYANAMMA INSTITUTE OF TECHNOLOGY & SCIENCE
(For Women) (AUTONOMOUS)**


Shaikpet, Hyderabad – 500104

**One-Week offline Faculty Development Program on FPGA & DSP Trainer Kits
Conducted from 26th July to 2nd August 2021**

S.No	Name of the document	Status
1.	Proposal by the Coordinator/Convenor to Principal along with budget, and permission for the venue.	Yes
2.	Invitation to the resource person	No
3.	Acceptance along with the bio-data	No
4.	Poster/brochure/Banner (Not photo)	Yes
5.	Programme schedule	Yes
6.	Circular	Yes
7.	Registration/Attendance of the participants	Yes
8.	Certificates (sample)	Yes
9.	All participants filled Feedback, analysis – action	Yes
10.	Resource person filled Feedback – plan of action	NA
11.	One page photos with atleast 6 with description as per the format	Yes
12.	Budget utilisation sheet	NA
13.	Press note/Newspaper publication	NA
14.	Single page report as per the format	Yes


Workshop Coordinator


Dept HOD


PRINCIPAL
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Shaikpet, Hyderabad - 500 104

**One week Offline Faculty Development
Training Programme on FPGA, DSP trainer kits
for the Faculty of Electrical and Electronics
Engineering Department**



J. Narayanamma Institute of Technology & Science (For Women)

ABOUT THE INSTITUTE

G. Narayanamma Institute of Technology & Science, a leading Engineering college in Hyderabad for women, was founded by late Sri G. Pulla Reddy garu in 1997, with an objective of providing excellent learning facilities for women to pursue education in Engineering since two decades. The aim is to promote Technical Education among women to enhance and build-up a new generation of thinkers, innovators and planners in the realms of Science and Technology. GNITS received UGC autonomous status for 10 years from 2018 and is affiliated to Jawaharlal Nehru Technological University (JNTU-H), Hyderabad. It is approved by All India Council for Technical Education (AICTE), accredited by NAAC & NBA (AICTE).

ABOUT THE ELECTRICAL AND ELECTRONICS DEPARTMENT

The Department of Electrical & Electronics Engineering was established and moulded in 1997 keeping in view, the broad objectives of technical education in general and specific needs of Power Engineers in particular. The Department has been accredited by Institution of Engineers in 2005. The Department has qualified & experienced faculty and adequate & well-furnished laboratories to cater to the needs of the students as per the university norms & curriculum. A centre called 'RENEWABLE ENERGY CLUB' under the aegis of Ministry of Renewable Energy Sources was started in Aug. 2006. The Department is responsible for Electrical maintenance of the entire Institution & also for the manning & operation of 750 KVA Diesel Power House.

Program Conveners:

Dr. N. Malla Reddy, Professor and HoD-EEE

Dr.P.Rama Krishna Reddy, Professor and EEE- R&D Coordinator

Faculty Program Coordinators :

Mrs. Gouthami Eragamreddy, Asst. Prof, EEE

Mr.P. Buchi babu, Asst.Prof. EEE

Dr.T.Hima Bindu Asst.Prof. EEE

Programme CONTENTS

- Basic Sessions on FPGA, VHDL Programing, DSP Trainer Kits
- Hands on sessions on
 - o 3 ph multilevel Inverter board
 - o 3 Ph Matrix Converter Board
 - o 5 Ph Inverter Power Module
 - o Intelligent Power Module
 - o 2407 DSP Trainer Kit

PROGRAM DATES: 26th July 2021 to August 2nd 2021
10:00 AM to 5:00 PM

MODE OF Sessions:

Offline and Online

Hands on sessions to the participants and

Online Live forecast Via Microsoft Teams
(21st to 26th June 2021)

RESOURCE PERSON :

Mr. Yuva Raja .S , S/W Team Head

Mr. Nanda Gopal, R&D Engineer

JSK Lab Instrument, Chennai

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4

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SHAIKPET, HYDERABAD – 500104
DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

Schedule: "One Week Offline Faculty Development Training Program on FPGA & DSP Trainer Kits"

Date	Time of Program	Topic for Discussion
(Monday) 26-07-2021	9:30 AM to 12:30 PM	Introduction of Speaker to Department
	1:30 PM to 3:30 PM	Integration of Renewable Energy Sources using FPGA
(Tuesday) 27-07-2021	9:30 AM to 12:30 PM	Introduction to FPGA Programming
	1:30 PM to 3:30 PM	A Hands on Session on Counter Wave generation
(Wednesday) 28-07-2021	9:30 AM to 12:30 PM	A Hands on Session Triangular and Square wave generation using FPGA and Xilinx
	1:30 PM to 3:30 PM	Simulation of Full bridge inverter using Xilinx on Spartan 6 Board
(Thursday) 29-07-2021	9:30 AM to 12:30 PM	Application of SPWM Technique to Full Bridge Inverter using FPGA & Xilinx
	1:30 PM to 3:30 PM	Application of SVPWM Technique to Full Bridge Inverter using FPGA & Xilinx
(Friday) 30-07-2021	9:30 AM to 12:30 PM	Speed control of Induction motor using Intelligent power Module and FPGA Spartan 6 board.
	1:30 PM to 3:30 PM	
(Saturday) 31-07-2021	9:30 AM to 12:30 PM	Introduction to DSP Programming
	1:30 PM to 3:30 PM	A Hands on Session on DSP Trainer kit
(Monday) 02-08-2021	9:30 AM to 12:30 PM	Implementation of Spwm technique for Inverter using DSP kit
	1:30 PM to 3:30 PM	Valedictory Session

N. Mallekelly

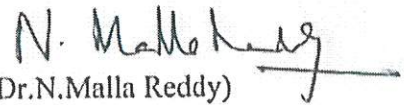
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Date: 23/07/2021

Circular

EEE Department is organizing a “One-Week offline Faculty Development Training Program on FPGA and DSP Trainer kits” scheduled from 26/07/2021 to 02/08/2021. In these regard i request all the faculty members,PG students to participate and utilize the program for Research and Academic aspects.


(Dr.N.Malla Reddy)

HOD-EEE

To
Coordinate among the
Staff + students




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 DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

Workshop Title: One Week Offline Faculty Development Training Program on FPGA & DSP Trainer Kits

Date: 26-07-2021 to 02-08-2021

Total Registered candidates:

S.No	Name of Faculty/Student	26/07/2021	27/07/2021	28/07/2021	29/07/2021	30/07/2021	31/07/2021	02/08/2021
1.	E. Gouthami	ED	ED	ED	ED	ED	ED	ED
2.	R. Nageswara Rao	R.N.R	R.N.R	R.N.R	R.N.R	R.N.R	R.N.R	R.N.R
3.	P.V.S.A. Pasumala	P.V.	P.V.	P.V.	P.V.	P.V.	P.V.	P.V.
4.	Dr. Himabindu.T	Dr.H	Dr.H	Dr.H	Dr.H	Dr.H	Dr.H	Dr.H
5.	G. Sujatha	G.S	G.S	G.S	G.S	G.S	G.S	G.S
6.	K.V. Somya	K.V.S	K.V.S	K.V.S	K.V.S	K.V.S	K.V.S	K.V.S
7.	P. Tejaswi	P.T	P.T	P.T	P.T	P.T	P.T	P.T
8.	Y. Priyanka	Y.P	Y.P	Y.P	Y.P	Y.P	Y.P	Y.P
9.	G. Annapurna	G.A	G.A	G.A	G.A	G.A	G.A	G.A
10.	DR.S.S.Tulasi Rao	DR.S	DR.S	DR.S	DR.S	DR.S	DR.S	DR.S
11.	APRIL Reddy	APR	APR	APR	APR	APR	APR	APR
12.	G. Ramana Reddy	G.R	G.R	G.R	G.R	G.R	G.R	G.R
13.	K. Sumanalatha	K.S	K.S	K.S	K.S	K.S	K.S	K.S
14.	K. KIRAN HANA MURTHY	K.K	K.K	K.K	K.K	K.K	K.K	K.K
15.	P. SANIRANJAN LOHAR	P.S	P.S	P.S	P.S	P.S	P.S	P.S


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 DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

S.No	Name of Faculty/Student	26/07/2021	27/07/2021	28/07/2021	29/07/2021	30/07/2021	31/07/2021	02/08/2021
1.	M. Archana Sai	Subbarao	Subbarao	Subbarao	Subbarao	Subbarao	Subbarao	Subbarao
2.	Ramyach (20251D5401)	Ch. Ranjya	Ch. Ranjya	Ch. Ranjya	Ch. Ranjya	Ch. Ranjya	Ch. Ranjya	Ch. Ranjya
3.	D. Renu Sri (20251D5402)	D. Renu Sri	D. Renu Sri	D. Renu Sri	D. Renu Sri	D. Renu Sri	D. Renu Sri	D. Renu Sri
4.	Chaitanya (20251D5403)	Chaitanya	Chaitanya	Chaitanya	Chaitanya	Chaitanya	Chaitanya	Chaitanya
5.	Anjali (20251D5404)	Anjali	Anjali	Anjali	Anjali	Anjali	Anjali	Anjali
6.	S. SHRUTHI (20251D5405)	S. Shradha	S. Shradha	S. Shradha	S. Shradha	S. Shradha	S. Shradha	S. Shradha
7.	S. Mounica (20251D5406)	Mounica	Mounica	Mounica	Mounica	Mounica	Mounica	Mounica
8.	Varshini (20251D5407)	Varshini	Varshini	Varshini	Varshini	Varshini	Varshini	Varshini
9.	Dakshayani (20251D5408)	P. Dakshayani	P. Dakshayani	P. Dakshayani	P. Dakshayani	P. Dakshayani	P. Dakshayani	P. Dakshayani
10.	Sonathoshi (1#251D5401)	Sonathoshi	Sonathoshi	Sonathoshi	Sonathoshi	Sonathoshi	Sonathoshi	Sonathoshi

N M M Reddy
 Dr. N Malla Reddy
 (HOD-EEE)

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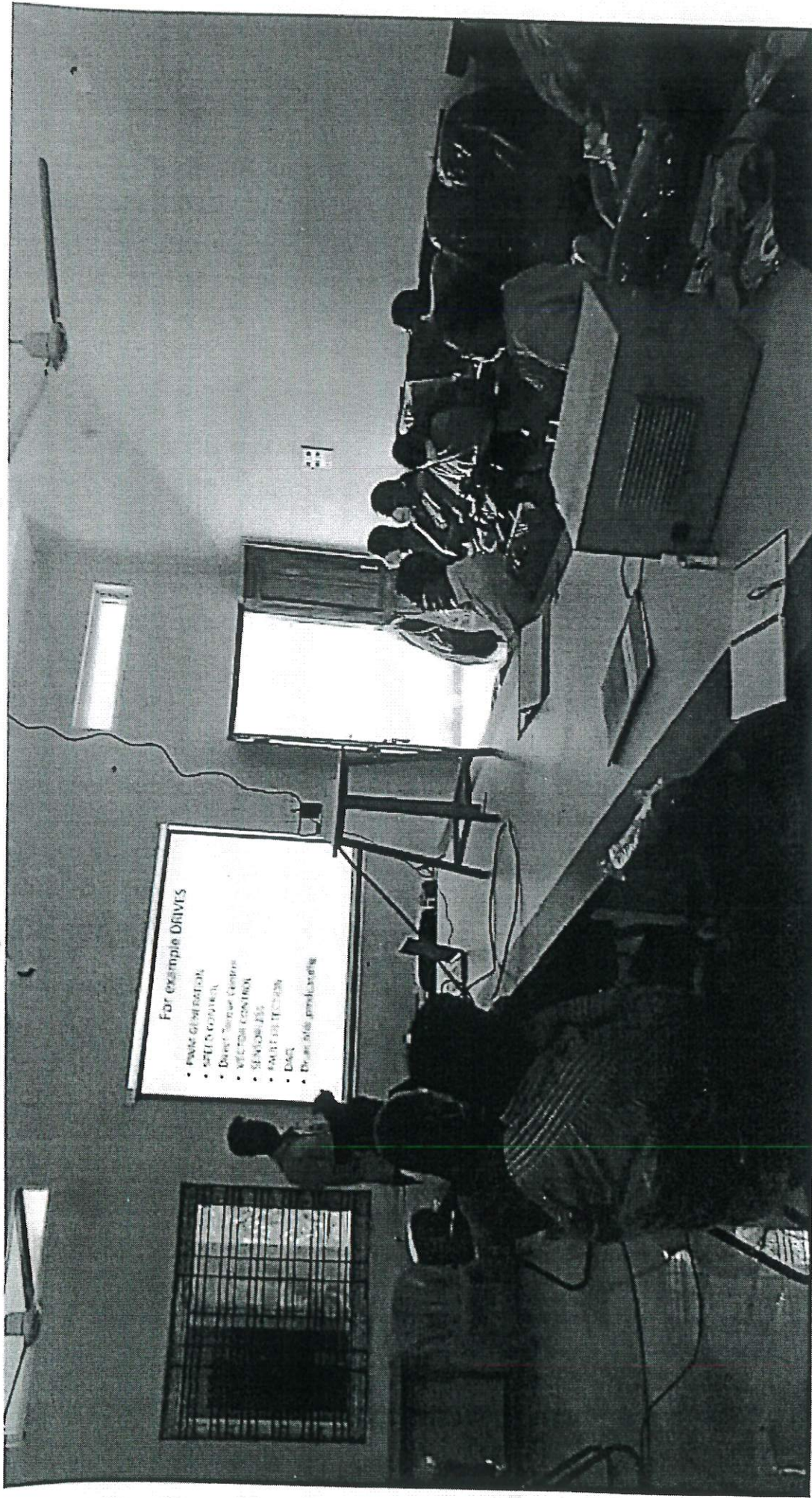


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For example DRIVES

- PWM GENERATION
- STEADY STATE
- Drive Train Control
- VECTOR CONTROL
- SPEED FEEDBACK
- DTC
- Drive shaft manufacturing



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Shaikpet, Hyderabad-500 104. Telangana

Department of Electrical & Electronics Engineering
Certificate of Participation

This is to certify that Ms/Mr.....**C.H.RAMYA**..... of Class**M.TECH**.....

Regd. No.**20251D5401**.....has participated in "One week Offline FDP on FPGA ,DSP

Trainer kits" from 26/7/2021 to 02/08/2021 in EEE Department GNITS.


Mrs. E. Gouthami

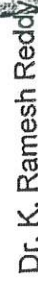
Faculty Coordinator


Mr. P. Buchibabu

Faculty Coordinator


Dr. N. Malla Reddy

HOD-EEE


Dr. K. Ramesh Reddy

Principal


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Department of Electrical & Electronics Engineering

Feedback from of Participants for Workshop Titled

One Week Offline Faculty Development Training Program on FPGA & DSP Trainer Kits

Date:

1) Rate Sessions



2) Rate the content based on your expectation



3) How much Knowledge you grabbed from the sessions



4) Do you recommend these sessions to your friends



5) Do you recommend these sessions to Continue



6) Comments /
Suggestions

..... It is more beneficial to
..... conduct such kind of informative workshops
.....

Name and Roll No: *M. Archana Sai (2025105405)*

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Department of Electrical & Electronics Engineering

Feedback from of Participants for Workshop Titled

One Week Offline Faculty Development Training Program on FPGA & DSP Trainer Kits
Date:

- 1) Rate Sessions ★1 ★2 ★3 ★4 ★5 ✓
- 2) Rate the content based on your expectation ★1 ★2 ★3 ★4 ✓ ★5
- 3) How much Knowledge you grabbed from the sessions ★1 ★2 ★3 ★4 ✓ ★5
- 4) Do you recommend these sessions to your friends ★1 ★2 ★3 ★4 ★5 ✓
- 5) Do you recommend these sessions to Continue ★1 ★2 ★3 ★4 ★5 ✓

6) Comments /
 Suggestions *Workshop was excellent*

.....

.....

Name and Roll No: *P. Dakshayani (2025105410)*

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Department of Electrical and Electronics Engineering
SHAIKPET, HYDERABAD-104

Detail Report of Workshop

Title of Workshop: One Week Offline Faculty Development Training Program on FPGA & DSP Trainer Kits.

Resource person: 1. Mr. S. Yuva Raja, S/W Team Head.
2. Mr. Nanda Gopal, R& D Engineer.

Coordinators: 1. Mrs. E. Gouthami, Asst-Prof, EEE Dept.
2. Mr. P. Buchibabu, Asst-Prof, EEE Dept.
3. Dr. T. Hima Bindu, Asst-Prof, EEE Dept.

Duration: From 26/07/2021 to 02/08/2021.

No. of Participants attended this workshop: 31

Software Tools: Xilinx Simulation Software.

Hardware tools: FPGA & DSP Kits.

About Workshop:

Data processing, system control and real-time monitoring have become indispensable to the modern industrial environment. Digital electronics has rendered the widely used analog controllers obsolete. Field programmable controllers like FPGA have introduced versatility and flexibility in control. While many of us may be stuck behind age-old microcontrollers, Currents provides us the opportunity to enter the digital era. Using FPGA, we can build your own digital circuits; we can simulate processors and interface memory devices to our ultimate control platform that is FPGA. This workshop is aimed to expose the faculty members and PG students in basic digital electronics, encoding and decoding tasks using HDL, and bolstering you with the ability to implement digital blocks as well as processor blocks. Participants will also learn memory device interfacing and file accessing. This workshop would also serve as a valuable bridging course to those aiming towards specializing in Power Electronics.


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