

DEPT. OF ELECTRONICS & COMMUNICATION ENGG

CERTIFICATE

THIS IS TO CERTIFY THAT THE MINI PROJECT ENTITLED

LOW POWER HIGH PERFORMANCE WALLACE TREE MULTIPLIER

IS THE BONAFIDE WORK OF

J. Sri Vaishnavi (19251A0481)

Muvva Hasmitha (19251A0498)

Bhavana Marpadaga (20255A0407)

SUBMITTED IN PARTIAL FULFILLMENT OF THE REQUIREMENT FOR THE AWARD OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS AND COMMUNICATION ENGINEERING DURING THE YEAR 2022-2023



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