

DEPT. OF ELECTRONICS & COMMUNICATION ENGG

CERTIFICATE

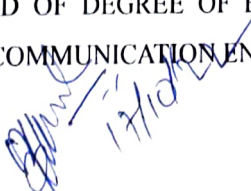
THIS IS TO CERTIFY THAT THE MINI PROJECT ENTITLED


**IMPLEMENTATION OF DIGITAL ALARM CLOCK USING
ARTIX-7 FPGA**

IS THE BONAFIDE WORK OF

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SUBMITTED IN PARTIAL FULFILMENT OF THE REQUIREMENT FOR THE
AWARD OF DEGREE OF BACHELOR OF TECHNOLOGY IN ELECTRONICS
AND COMMUNICATION ENGINEERING DURING THE YEAR 2022-2023


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