



IEEE Hyderabad Section

# ICMAACC 2022

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**Vani Dasu**

**has presented the research paper titled**

**Implementation of Unbiased Rounding for 64-bit Floating Point Adder**

**Authored by**

**Vani Dasu and K Ragini**

**at the**

**International Conference on Recent Trends in Microelectronics,  
Automation, Computing and Communication Systems**

**ICMAACC**  
hosted by

**VNR Vignana Jyothi Institute of Engineering & Technology**

**Prof. Y Padma Shayi**  
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